

Created 7/21/2011 by Raymond Kawakami
 IBM Thinkpad 600 BIOS IBET54WW POST/BIOS Version 1.49
 Byte changes between defaults (BIOS Initialize), disabling IR and enabling IR, using PS2.exe
 It has been confirmed that changing byte 22 from C9 to C8 will be enough to disable the IR port

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
0	36	26	28	94	21	43	05	20	07	11	26	02	50	A4	00	00	Initialized in BIOS
	47	26	42	94	21	43	05	20	07	11	26	02	50	A4	00	00	PS2 IR DISABLE command
	00	26	52	94	21	43	05	20	07	11	26	02	50	A4	00	00	PS2 IR ENABLE command
10	40	00	00	00	03	80	02	FF	FF	00	00	00	00	33	83	12	Initialized in BIOS
	40	00	00	00	03	80	02	FF	FF	00	00	00	00	33	83	12	PS2 IR DISABLE command
	40	00	00	00	03	80	02	FF	FF	00	00	00	00	33	83	12	PS2 IR ENABLE command
20	00	08	C9	39	FC	01	11	00	34	E1	03	80	FD	10	08	48	Initialized in BIOS
	00	08	C8	39	FC	01	11	00	34	E1	03	80	FD	10	08	47	PS2 IR DISABLE command
	00	08	C9	39	FC	01	11	00	34	E1	03	80	FD	10	08	48	PS2 IR ENABLE command
30	FF	FF	20	86	FF	B4	FD	10	00	00	00	00	00	00	00	00	Initialized in BIOS
	FF	FF	20	86	FF	B4	FD	10	00	00	00	00	00	00	00	00	PS2 IR DISABLE command
	FF	FF	20	86	FF	B4	FD	10	00	00	00	00	00	00	00	00	PS2 IR ENABLE command
40	17	88	00	01	00	03	08	04	00	08	70	40	00	FF	FF	FF	Initialized in BIOS
	17	88	00	01	00	03	08	04	00	08	70	40	00	FF	FF	FF	PS2 IR DISABLE command
	17	88	00	01	00	03	08	04	00	08	70	40	00	FF	FF	FF	PS2 IR ENABLE command
50	00	02	50	14	0C	00	F0	14	0C	00	F0	14	14	00	FF	FF	Initialized in BIOS
	00	02	50	14	0C	00	F0	14	0C	00	F0	14	14	00	FF	FF	PS2 IR DISABLE command
	00	02	50	14	0C	00	F0	14	0C	00	F0	14	14	00	FF	FF	PS2 IR ENABLE command
60	FF	00	06	21	00	11	04	01	96	80	30	0E	00	00	04	9E	Initialized in BIOS
	FF	00	06	21	00	11	08	01	96	80	30	0E	00	00	04	9E	PS2 IR DISABLE command
	FF	00	06	21	00	11	04	01	96	80	30	0E	00	00	04	9E	PS2 IR ENABLE command
70	00	06	FC	00	00	00	00	02	34	40	02	83	49	53	B2	00	Initialized in BIOS
	02	06	FC	00	00	00	00	02	34	08	02	83	49	53	B2	00	PS2 IR DISABLE command
	02	06	FC	00	00	00	00	02	34	08	02	83	49	53	B2	00	PS2 IR ENABLE command

IBM 600 Base 128 CMOS